Synopsys- Boxborough, MA

Our mixed signal design team is looking for Co-ops, to work in our Boxborough MA office, for a period of at least 6 months, starting in the early summer of 2015 and ending in Dec 2015.

The co-ops will be involved in the design and implementation of high speed logic and/or custom circuits for high-performance mixed signal designs. Candidates should have knowledge of basic circuits, logic design and a scripting language (e.g. Perl, Python or Tcl). Knowledge of placement and route tools, Verilog, low power design techniques, gate-level design/verification, timing analysis, electrical reliability and physical design is a plus.

No prior design experience is required for this position and undergraduate students are encouraged to apply. Candidates must have completed at least 2 semesters of university coursework in either Electrical Eng, Computer Eng, Comp Sci, Applied Physics or similar technical field.

Applicants should send resumes to Gerard Vernes at Gerard.Vernes@synopsys.com with the subject as “Summer/Fall 2015 Design Co-op”. 