INTEL

Logic / Validation Engineer

Location: Hudson, Massachusetts
(Research & Development Center - Data Center and Platform Engineer Groups)

Description:
For 45 years Intel has been inventing, pioneering and producing microprocessor technology for the modern world. A smart evolution is underway and Intel technologies -- in massive data centers, sleek 2 in 1 devices and portable all-in-one computers, phones and tablets, and the tiniest sensors -- are shaping an era when everything computes and connects, from automobiles and vending machines to automated factories and cities.

Intel has had a major research and development presence in Hudson, Massachusetts since 1998. The technology created in Hudson powers high-performance servers, notebook computers, and mobile products around the world.

Hudson-based design teams in Intel's Data Center and Platform Engineer Groups are currently looking for the best and brightest candidates to expand our capabilities. These teams are primarily responsible for enterprise CPUs, accelerators and the fabric that connects them in data centers, high-performance computing clusters, work stations and customer-tailored systems.

This position provides an exciting and challenging technical opportunity in logic design, Register Transfer Level (RTL) coding, and simulation to generate IP libraries, functional units, and sub-systems for system on a chip designs. You will participate in the development of architecture and micro-architecture specifications for the logic components along with ensuring the logical design satisfies this specification. Candidates will develop and use software to validate model behavior using focused and random test cases, analyzing coverage and then debug the failure cases. You will provide IP integration support to customers and represent your team in project- and division-wide forums. Tight collaboration is expected between silicon architects, logic, validation and circuit design teams.

Minimum Qualifications:
- Must have or be on track towards a BS or MS in Electrical Engineering, Computer Engineering or Computer Science with graduation in winter 2014/5 (preferred) or spring 2015
- Relevant coursework in digital logic, microprocessor design, computer architecture, and software/programming.
- Minimum 3 month experience with VHDL/Verilog/System Verilog RTL coding

Preferred Qualifications:
- Experience with validation concepts
- Experience with digital/ASIC/SOC design
- VLSI coursework or projects

Qualifications listed above can be obtained through your school work, classes, research or relevant previous job and internship experiences.
Some positions involve work on a U.S. Government contract which may impose certain security requirements: If you are a U.S. citizen, the government may require that you certify that you are a U.S. citizen. If you are not a U.S. citizen, the government may require you to pass a security check before you can be approved to work on the project. Please note that any offer by Intel for this position is conditioned upon meeting and/or passing the U.S. government's security check requirements should the government impose these requirements.

Please send resumes to:
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